

SSCE3V322L1

The SSCE3V322L1 is a bi-directional TVS diode. It is designed with AF process TVS technology to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space comes at a premium.

It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD(electrostatic discharge), and EFT (electrical fast transients).

Features

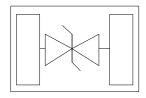
- Peak Power Dissipation –80W (8 x 20 us Waveform)
- Stand-off Voltage: 3.3V
- Low capacitance for high-speed interfaces
- Protects I/O Port
- Low Clamping Voltage
- Low Leakage
- Meets MSL 1 Requirements
- ROHS compliant



DFN0603-2L

Main applications

- High Speed Line: USB1.0/2.0,VGA
- Serial and Parallel Ports
- Notebooks, Desktops, Servers
- Projection TV
- Cellular handsets and accessories
- Portable instrumentation
- Peripherals



Protection solution to meet

■ IEC61000-4-2 (ESD) ±30kV (air), ±30kV (contact)

Ordering Information

Device	Marking	Package	Qty per Reel	Reel Size	
SSCE3V322L1	3C	DFN0603	15,000pcs	7 Inch	



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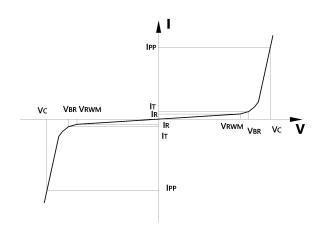
Maximum ratings (Tamb=25°C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit		
Peak Pulse Power (tp=8/20μs waveform)	\mathbf{P}_{PP}	80	Watts		
Peak Pulse Current(tp=8/20μs waveform)	Ірр	8	A		
ESD Rating per IEC61000-4-2: Contact		30	VV		
Air		30	KV		
Lead Soldering Temperature	$T_{\rm L}$	260 (10 sec.)	$^{\circ}$		
Operating Temperature Range	Tı	- 55 ∼ 125	$^{\circ}$		
Storage Temperature Range	Tstg	-55 ~ 150	$^{\circ}$		
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	$^{\circ}$		

Electrical characteristics (Tamb=25°C Unless Otherwise Specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V _{RWM}	Reverse Working Voltage				3.3	V
VBR	Breakdown Voltage	$I_T = 1 \text{mA},$	3.8			V
IR	Reverse Leakage Current	$V_{RWM} = 3.3V$,		0.01	0.2	uA
		$I_{PP} = 1A$, $tp = 8/20 \mu s$,		6		V
Vc	Clamping Voltage	$I_{PP} = 5A$, $tp = 8/20 \mu s$,		8		V
		$I_{PP} = 8A$, $tp = 8/20 \mu s$,		10	12	V
C _J	Junction Capacitance	$V_R = 0V$, $f = 1MHz$,		13	25	pF

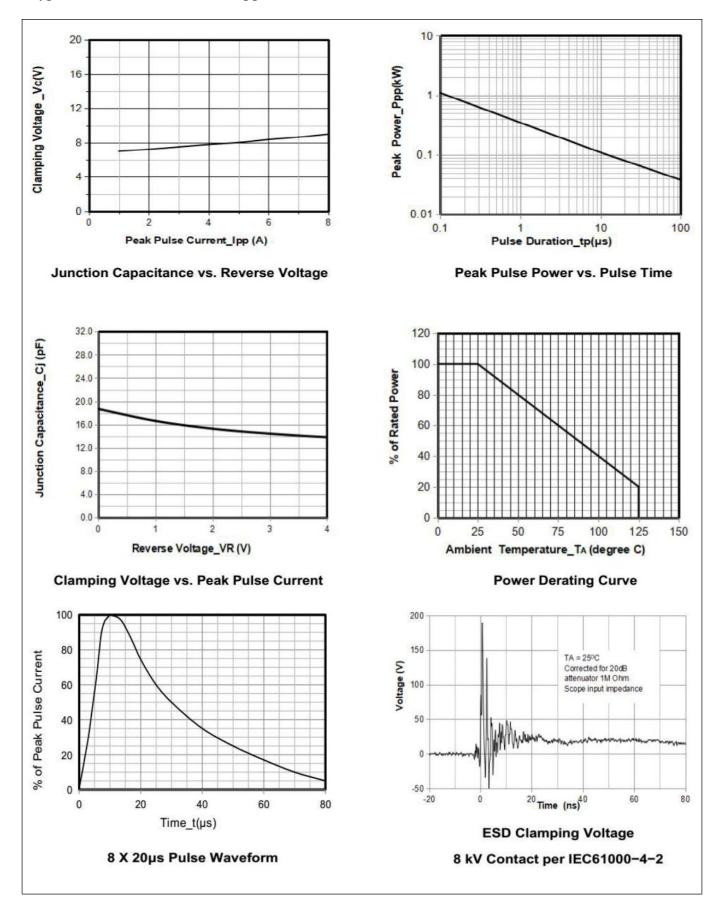
Symbol	Parameter
Vrwm	Working Peak Reverse Voltage
VBR	Breakdown Voltage @ IT
V _C	Clamping Voltage @ IPP
I_{T}	Test Current
Irm	Leakage current at VRWM
Ірр	Peak pulse current
Co	Off-state Capacitance
C_{J}	Junction Capacitance



SSC-V1.0 <u>www.afsemi.com</u> Analog Future



Typical electrical characterist applications





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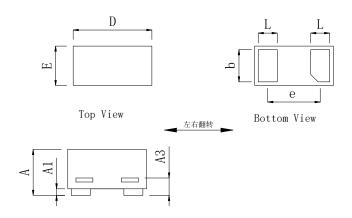
Package Information

DFN0603-2L

Mechanical Data

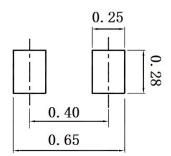
Case: DFN0603-2L

Case Material: Molded Plastic. UL Flammability

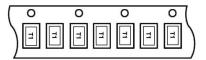


DIM	Millimeters					
DIM	Min	Max				
A	0.230	0.330				
A1	0.000	0.050				
A3	0.102REF					
D	0.550	0.650				
E	0.250	0.350				
b	0.210	0.275				
L	0.120	0.175				
e	0.40BSC					

Recommended Pad outline

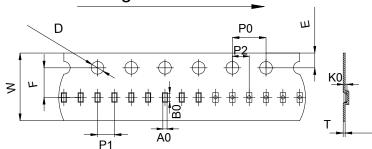


Device Orientation in Tape



DFN0603 Reel Dim

Progressive direction



PACKAGE	W	E	F	P0	D	P2	P1	T	A0	В0	K0
8mm	8mm	1.75mm	3.5mm	4mm	1.5mm	2mm	2mm	0.23mm	0.34mm	0.67mm	0.4mm
DFN0603	±0.1	±0.1	±0.05	±0.1	±0.1	±0.05	±0.1	±0.02	±0.05	±0.05	±0.05





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